W-WIN-01

W-WIN-01 Wireless window contact

FW version 1.01
250 registers can be read/write at the same time
register access only (functions 3 and 16), coil access not allowed

name	address	type	description	default value
Modbus ID	1	R	modbus map identifier	0x510
FW num	2	R	Firmware compatibility version	101
Status	3	RW	Bit 8 set: write content of RAM into EEPROM	0
Device_id	6	R	Device identifier .. used by bootloader	1216
HW num	7	R	PCB version	10
Bootloader FW num	8	R	Version of bootloader (0 if application runs)	
Name 0	9	RWE	user name, 16 chars	WIN 1
Name 1	10	RWE		
Name 2	11	RWE		
Name 3	12	RWE		
Name 4	13	RWE		
Name 5	14	RWE		
Name 6	15	RWE		
Name 7	16	RWE		
RF address Lo	17	R		0x01200000 – 0x012FFFFF
RF address Hi	18	R		
RF key 0	19	RWE	16 byte AES key	MIKROKLIMA1234AB
RF key 1	20	RWE		
RF key 2	21	RWE		
RF key 3	22	RWE		
RF key 4	23	RWE		
RF key 5	24	RWE		
RF key 6	25	RWE		
RF key 7	26	RWE		
RF frequency	27 LSB	RWE	Communication frequency 0..868.95 MHz, 100 kBit 1..868.3 MHz, 32.768 kBit 2..868.1 MHz, 100 kBit 3..869.525 MHz, 100 kBit 4..868.3 MHz, 38.400 kBit	0 (868.95 MHz)
RF power	27 MSB	RWE	Transmission power 0 .. 13 dBm 1 .. 13 dBm 2 .. 10 dBm 3 .. 5 dBm 4 .. 0 dBm 5 .. -5 dBm 6 .. -10 dBm 7 .. -15 dBm 8 .. -20 dBm	2 (+ 10 dBm)
EEPROM writes	28	R	number of writes into FLASH	
Uptime Lo	29	R	uptime in seconds	Transmitted spontaneously every 120 seconds
Uptime Hi	30	R		Transmitted spontaneously every 120 seconds

RF background RSSI	31 LSB	R	signed char background rssi -128 .. +20 dBm	
Vbat	32 LSB	R	x 0.1V Battery voltage	Transmitted spontaneously by vbat change or every 120 seconds

Bat state	32 MSB	R	Bit 0..3 battery state, x 10 %
			   0..10 = 0%..100%
			   15 not measured yet
			Bit 7 .. lowbat	

...	...	...	...	
Inputs state	51 LSB	R	Bit 0 .. actual state of window contact 0 window closed 1 window opened	Transmitted spontaneously by input/vbat/temperature change or every 60 seconds
Input on event	51 MSB	R	Bit 0 .. window opened now, register is cleared by read	Transmitted spontaneously by input/vbat/temperature change or every 60 seconds
Input off event	52 LSB	R	Bit 0 .. window closed now, register is cleared by read	Transmitted spontaneously by input/vbat/temperature change or every 60 seconds
Vcpu	52 MSB	R	x 0.1V CPU voltage	Transmitted spontaneously by input/vbat/temperature change or every 60 seconds
Actual CPU temperature	55	R	signed short x 0.01°C CPU temperature	Transmitted spontaneously by temperature change or every 120 seconds
CPU temperature correction	56	RWE	signed short x 0.01°C CPU temperature correction	0 (0°C)
CPU temperature change threshold	57	RWE	signed short x 0.01°C CPU temperature threshold	200 (2°C)
Input PRG UNIDIR	58	R	Bit 0..PRG Bit 1..UNIDIR	

R – register is read only
W – register is write only
RW – register is read/write
RWE – register is read from EEPROM, write to EEPROM